

DECENTRALIZED ROBUST FLOW CONTROLLER DESIGN FOR NETWORKS WITH MULTIPLE BOTTLENECKS

İnci Munyas Özen Yelbaşı Altuğ İftar

Department of Electrical and Electronics Engineering, Anadolu University, Eskişehir 26470, Turkey

{imunyas , oyelbasi , aiftar}@anadolu.edu.tr

Fax: 90 222 323 9501

Keywords: Communication networks, flow control, decentralized control, \mathcal{H}^∞ control, time-delay systems.

Abstract

Decentralized rate-based flow controller design in multi-bottleneck networks is considered. An \mathcal{H}^∞ problem is formulated to find decentralized controllers which are robust to time-varying uncertain multiple time-delays in different channels. A suboptimal solution to this problem is found and the implementation of the decentralized controllers at different bottleneck nodes is presented. Besides robustness, the controllers also satisfy tracking and weighted fairness requirements. A number of simulations are also included to illustrate the time-domain performance of the proposed controllers.

1 Introduction

A communication network should be able to deliver data packets with minimum loss and as soon as possible to the destination. To achieve this aim the resources of the network should be used efficiently. Flow control is one of the basic resource management tools in high-speed communication networks. Flow control always involves a direct feedback from the receiver (bottleneck nodes) to the sender (sources and the other nodes sending data to that node). The type of the flow control is determined by the type of the feedback scheme used; i.e., it can be either *rate-based* or *window-based*. However, for both cases, the existence of the time-delays in the data-flow makes the design of the controller a difficult task. Since the controller is to be implemented at the bottleneck node, the time a command signal for a rate is issued and the time this rate is set are different, which corresponds to the *backward delay* from the bottleneck node to the source node. Besides, another time-delay occurs which is the time needed for the data packets sent with the new rate to reach the bottleneck node; i.e. the *forward delay*, from the source node to the bottleneck node. The *round-trip delay* from the control input to the regulated output is the sum of these two delays. It should also be considered that these time-delays are usually uncertain and time-varying. Since there usually are more than one source and node connected to a bottleneck, these delays are multiple. Many approaches to the flow controller design problem have been presented in the literature (see [1, 4] and references therein). Among these, the time-varying uncertainties in the time-delays have been explicitly considered in [4] and a rate-based flow controller have been designed, using \mathcal{H}^∞ methods, which is robust to uncertain time-

varying multiple time-delays in different channels. In [4], however, only the single-bottleneck case has been considered. The multi-bottleneck case has been considered in [1], where it has been shown that decentralized flow controllers can be designed to solve the same problem in this case. The controller derivation, however, has not been considered in [1]. In the present paper, we re-present the problem of designing decentralized rate-based flow controllers, which are robust against time-varying uncertainties in time-delays in different channels, in the case of multi-bottleneck networks. We show the derivation of the controllers and present their implementation. We also implement the proposed controllers and present a number of simulation results to show the time-domain performance of the proposed controllers in certain realistic cases.

2 Problem Statement

In this work, we consider a network which consists of n bottleneck nodes and n_i sources feeding the i^{th} bottleneck node. Note that, if any physical source sends data to more than one bottleneck node, this source may be considered as a different source for each bottleneck node for the purpose of controller design. We also assume that, besides the sources, each bottleneck can also send data through other bottlenecks; i.e., each bottleneck can also be a source for the other bottlenecks. The problem is to design a decentralized controller to be implemented at each bottleneck node, to regulate the data sending rates of the sources and the other bottleneck nodes to that node.

The dynamics of the queue length at the i^{th} bottleneck node can be described as

$$\dot{q}_i(t) = \sum_{k=1}^{n_i} r_{i,k}^b(t) + \sum_{k=1, k \neq i}^n \rho_{k,i}^b(t) - c_i(t) - \sum_{k=1, k \neq i}^n \rho_{i,k}^s(t) \quad (1)$$

where $q_i(t)$ is the queue length at the i^{th} bottleneck node at time t ($i = 1, 2, \dots, n$); $r_{i,k}^b(t)$ is the rate of data received at the i^{th} bottleneck node from the k^{th} source of the i^{th} bottleneck node at time t ($i = 1, 2, \dots, n, k = 1, 2, \dots, n_i$); $\rho_{k,i}^b(t)$ is the rate of data received at the i^{th} bottleneck node at time t from the k^{th} bottleneck node ($i = 1, 2, \dots, n, k = 1, 2, \dots, n, i \neq k$); $c_i(t)$ is the outgoing flow rate, except for the flow going to the other bottleneck nodes, of the i^{th} bottleneck node at time t ($i = 1, 2, \dots, n$); and $\rho_{i,k}^s(t)$ is the rate of data sent from the i^{th} to the k^{th} bottleneck node at time t ($i = 1, 2, \dots, n, k = 1, 2, \dots, n, i \neq k$). Since there is a time-varying backward delay, $\phi_{i,k}^b(t)$, between the k^{th} and the i^{th} bottleneck nodes,

we have $\rho_{i,k}^s(t) = \rho_{i,k}(t - \phi_{i,k}^b(t))$, where $\rho_{i,k}(t)$ is the flow rate command at time t for the flow from the i^{th} to the k^{th} bottleneck node ($i = 1, 2, \dots, n, k = 1, 2, \dots, n, i \neq k$), which is to be computed (by the controller to be designed) at the k^{th} bottleneck node. Data receiving rates, $r_{i,k}^b(t)$ and $\rho_{k,i}^b(t)$, on the other hand, can be found as (see [4, 1])

$$r_{i,k}^b(t) = \begin{cases} (1 - \delta_{i,k}^{rf}(t))r_{i,k}(t - \tau_{i,k}^f(t)), & t - \tau_{i,k}^f(t) \geq 0 \\ 0, & t - \tau_{i,k}^f(t) < 0 \end{cases} \quad (2)$$

$$\rho_{i,k}^b(t) = \begin{cases} (1 - \delta_{i,k}^{\rho f}(t))\rho_{i,k}(t - \phi_{i,k}^f(t)), & t - \phi_{i,k}^f(t) \geq 0 \\ 0, & t - \phi_{i,k}^f(t) < 0 \end{cases} \quad (3)$$

where $r_{i,k}(t)$ is the flow rate command at time t for the flow from the k^{th} source of the i^{th} bottleneck node to the i^{th} bottleneck node ($i = 1, 2, \dots, n, k = 1, 2, \dots, n, i \neq k$), which is to be computed (by the controller to be designed) at the i^{th} bottleneck node; $\tau_{i,k}^f(t)$ is the forward time-delay from the k^{th} source of the i^{th} bottleneck node to the i^{th} bottleneck node at time t ($i = 1, 2, \dots, n, k = 1, 2, \dots, n, i \neq k$) and $\delta_{i,k}^{rf}(t)$ is its time-varying uncertain part (i.e., $\tau_{i,k}^f(t) = h_{i,k}^{rf} + \delta_{i,k}^{rf}(t)$, where $h_{i,k}^{rf}$ is the time-invariant nominal part); and $\phi_{i,k}^f(t)$ is the forward time-delay from the i^{th} to the k^{th} bottleneck node at time t ($i = 1, 2, \dots, n, k = 1, 2, \dots, n, i \neq k$) and $\delta_{i,k}^{\rho f}(t)$ is its time-varying uncertain part (i.e., $\phi_{i,k}^f(t) = h_{i,k}^{\rho f} + \delta_{i,k}^{\rho f}(t)$, where $h_{i,k}^{\rho f}$ is the time-invariant nominal part).

The round-trip delay at time t for the flow from the k^{th} source of the i^{th} bottleneck node to the i^{th} bottleneck node is given as $\tau_{i,k}(t) = \tau_{i,k}^b(t) + \tau_{i,k}^f(t) = h_{i,k}^r + \delta_{i,k}^r(t)$, where $h_{i,k}^r$ is the time-invariant nominal part and $\delta_{i,k}^r(t)$ is the time-varying uncertain part. Similarly, the round-trip delay at time t for the flow from the i^{th} to the k^{th} bottleneck node is given as $\phi_{i,k}(t) = \phi_{i,k}^b(t) + \phi_{i,k}^f(t) = h_{i,k}^\rho + \delta_{i,k}^\rho(t)$, where $h_{i,k}^\rho$ is the time-invariant nominal part and $\delta_{i,k}^\rho(t)$ is the time-varying uncertain part. It is assumed that the uncertainties satisfy the following;

$$|\delta_{i,j}^r(t)| < \delta_{i,j}^{r+}, \quad |\delta_{i,j}^r(t)| < \beta_{i,j}^r, \quad |\delta_{i,j}^{rf}(t)| < \beta_{i,j}^{rf}, \quad (4)$$

$$|\delta_{i,k}^\rho(t)| < \delta_{i,k}^{\rho+}, \quad |\delta_{i,k}^\rho(t)| < \delta_{i,k}^{\rho b+}, \\ |\delta_{i,k}^\rho(t)| < \beta_{i,k}^\rho, \quad |\delta_{i,k}^{\rho f}(t)| < \beta_{i,k}^{\rho f}, \quad (5)$$

for all t , for some $\delta_{i,j}^{r+} > 0, 0 < \beta_{i,j}^{rf} < \beta_{i,j}^r < 1, 0 < \delta_{i,k}^{\rho b+} < \delta_{i,k}^{\rho+}, 0 < \beta_{i,k}^{\rho f} < \beta_{i,k}^\rho < 1$ ($i = 1, 2, \dots, n, j = 1, 2, \dots, n, k = 1, 2, \dots, n, k \neq i$).

It should be noted that, in a real application, there also exist some *hard constraints*, such as non-negativity constraints and upper bounds on the queue lengths and on the data rates. However, in this work, we will assume that these hard constraints are always satisfied.

It can be shown that (see [1, 2]) our system is captured by the fictitious system shown in Figure 1, where

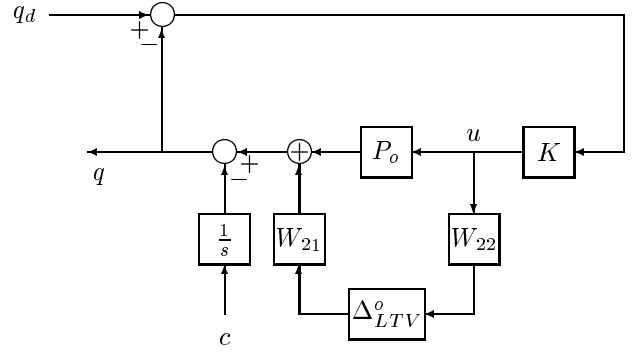


Figure 1: Fictitious system.

$q := [q_1 \ \dots \ q_n]^T$, where superscript T denotes the transpose, $q_d := [q_{d,1} \ \dots \ q_{d,n}]^T$, where $q_{d,i}$ is the desired queue length at the i^{th} bottleneck node ($i = 1, \dots, n$), $c := [c_1 \ \dots \ c_n]^T$, $u := [r^T \ \rho^T]^T$, where $r := [r_1^T \ \dots \ r_n^T]^T$, where $r_i := [r_{i,1} \ \dots \ r_{i,n_i}]^T$, $\rho := [\rho_1^T \ \dots \ \rho_n^T]^T$, where $\rho_i := [\rho_{1,i} \ \dots \ \rho_{i-1,i} \ \rho_{i+1,i} \ \dots \ \rho_{n,i}]^T$, $P_o(s) = \frac{1}{s} \bar{P} e^{-Hs} \hat{P}$ is the nominal plant, where

$$\hat{P} := \begin{bmatrix} I_m & 0 \\ 0 & \frac{1}{\sqrt{2}} I_{n(n-1)} \\ 0 & -\frac{1}{\sqrt{2}} I_{n(n-1)} \end{bmatrix}, \quad \text{where } I_k \text{ denotes the}$$

$k \times k$ identity matrix and $m := \sum_{i=1}^n n_i$, $H := \text{diag}(\bar{h})$, where $\bar{h} := [h^r \ h^\rho \ h^{\rho b}]$, where $h^r := [h_1^r \ \dots \ h_n^r]$, where $h_i^r := [h_{i,1}^r \ \dots \ h_{i,n_i}^r]$, $h^\rho := [h_1^\rho \ \dots \ h_n^\rho]$, where $h_i^\rho := [h_{1,i}^\rho \ \dots \ h_{i-1,i}^\rho \ h_{i+1,i}^\rho \ \dots \ h_{n,i}^\rho]$, $h^{\rho b}$ and $h_i^{\rho b}$ has the same structure as h^ρ and h_i^ρ , respectively.

$\bar{P} := [\bar{P}_r \ \sqrt{2}\bar{P}_\rho \ \sqrt{2}\bar{P}_{\rho b}]$, where $\bar{P}_r := \text{blockdiag}(\mathbf{1}_{n_1}, \dots, \mathbf{1}_{n_n})$, where $\mathbf{1}_k$ denotes the $1 \times k$ dimensional row vector of 1's, $\bar{P}_\rho := I_n \otimes \mathbf{1}_{n-1}$,

$$\bar{P}_{\rho b} := \begin{bmatrix} 0 & J_1 & J_1 & J_1 & \dots & J_1 & J_1 \\ J_1 & 0 & J_2 & J_2 & \dots & J_2 & J_2 \\ J_2 & J_2 & 0 & J_3 & \dots & J_3 & J_3 \\ \vdots & \vdots & \vdots & \vdots & \ddots & \vdots & \vdots \\ J_{n-1} & J_{n-1} & J_{n-1} & J_{n-1} & \dots & J_{n-1} & 0 \end{bmatrix},$$

where $J_i := [0_{1 \times (i-1)} \ 1 \ 0_{1 \times (n-i-1)}]$,

$$W_{22} := \begin{bmatrix} I_m & 0 \\ 0 & \frac{1}{\sqrt{2}} I_{n(n-1)} \\ 0 & \frac{1}{\sqrt{2}} I_{n(n-1)} \end{bmatrix}, \quad W_{21} \text{ is a weighting matrix}$$

which depends on the bounds in (4)–(5), K is the controller to be designed, and Δ_{LTV}^o represents an arbitrary linear time-varying system with \mathcal{L}^2 -induced norm less than 1 (see [2] for details).

For the system shown in Figure 1 to be robustly stable for all $\|\Delta_{LTV}^o\| < 1$, K should stabilize P_o and

$$\|W_{22}K(I + P_oK)^{-1}W_{21}\|_\infty \leq 1 \quad (6)$$

should be satisfied. It can be shown that (see [2] for details)

(6) is satisfied if

$$\left\| \xi \hat{P}K (I + P_o K)^{-1} \right\|_{\infty} \leq 1 \quad (7)$$

where $\xi(s) := \frac{1}{s}\xi_1 + \xi_2$, where ξ_1 and ξ_2 are constants which depend on the bounds in (4)–(5) (see [2]).

Next, as in [4], to guarantee tracking ($\lim_{t \rightarrow \infty} q_i(t) = q_{d,i}$) and good transient response, we formulate the problem

$$\text{minimize } \left\| W_1 (I + P_o K)^{-1} \right\|_{\infty} \quad (8)$$

over all controllers K stabilizing P_o , where $W_1(s) = \frac{1}{s^2}$. Then, by combining the robust stability (7) and nominal performance (8) conditions we define the following two-block \mathcal{H}^{∞} optimization problem:

$$\inf_{K \text{ stabilizing } P_o} \left\| \left[\begin{array}{c} W_1 (I + P_o K)^{-1} \\ \xi \hat{P}K (I + P_o K)^{-1} \end{array} \right] \right\|_{\infty} =: \gamma^{\text{opt}}. \quad (9)$$

3 Solution

To find a solution to the optimization problem (9), let $\hat{K} := \hat{P}K$ and note that $P_o K = \check{P}\hat{K}$, where $\check{P}(s) := \frac{1}{s}\check{P}e^{-Hs} = \begin{bmatrix} P^r(s) & P^{\rho}(s) & P^{\rho b}(s) \end{bmatrix}$, where $P^r(s) := \frac{1}{s}\check{P}_r e^{-H^r s}$, $P^{\rho}(s) := \frac{\sqrt{2}}{s}\check{P}_{\rho} e^{-H^{\rho} s}$, $P^{\rho b}(s) := \frac{\sqrt{2}}{s}\check{P}_{\rho b} e^{-H^{\rho b} s}$, where $H^r := \text{diag}(h^r)$, $H^{\rho} := \text{diag}(h^{\rho})$, and $H^{\rho b} := \text{diag}(h^{\rho b})$. Also let

$$Q := \hat{K} (I + \check{P}\hat{K})^{-1} =: \begin{bmatrix} Q^r \\ Q^{\rho} \\ Q^{\rho b} \end{bmatrix} \quad (10)$$

where the partitioning of Q corresponds to the partitioning of \check{P}^T .

Note that, P^r and P^{ρ} are block diagonal. Although $P^{\rho b}$ is not block diagonal, by permuting the columns of $I_{n(n-1)}$, we can find a non-singular matrix T , satisfying $T^T T = T T^T = I$, such that $\hat{P}^{\rho b} := P^{\rho b} T^T$ is block diagonal. Then, by defining $\hat{Q}^{\rho b} := T Q^{\rho b}$ (thus $P^{\rho b} Q^{\rho b} = \hat{P}^{\rho b} \hat{Q}^{\rho b}$), the optimization problem (9) can be rewritten as:

$$\begin{aligned} & \inf_{Q \in \mathcal{H}^{\infty}} \left\| \left[\begin{array}{c} W_1 (I - \check{P}Q) \\ \xi Q \end{array} \right] \right\|_{\infty} \\ & = \inf_{Q \in \mathcal{H}^{\infty}} \left\| \left[\begin{array}{c} W_1 (I - P^r Q^r - P^{\rho} Q^{\rho} - \hat{P}^{\rho b} \hat{Q}^{\rho b}) \\ \xi Q \end{array} \right] \right\|_{\infty} =: \gamma^{\text{opt}} \end{aligned} \quad (11)$$

In order to obtain a decentralized structure for K , we impose block diagonal structures $Q^r = \text{blockdiag}(Q_1^r, \dots, Q_n^r)$, $Q^{\rho} = \text{blockdiag}(Q_1^{\rho}, \dots, Q_n^{\rho})$, $\hat{Q}^{\rho b} = \text{blockdiag}(\hat{Q}_1^{\rho b}, \dots, \hat{Q}_n^{\rho b})$, where the structures correspond to the structures of P^{rT} , $P^{\rho T}$, $\hat{P}^{\rho b T}$, respectively. On noting that $\|\hat{Q}^{\rho b}\|_{\infty} = \|T Q^{\rho b}\|_{\infty}$, problem (11) now decomposes into the following problems:

$$\inf_{\hat{Q}_i \in \mathcal{H}^{\infty}} \left\| \left[\begin{array}{c} W_1 (1 - P_i^r Q_i^r - P_i^{\rho} Q_i^{\rho} - \hat{P}_i^{\rho b} \hat{Q}_i^{\rho b}) \\ \xi \begin{bmatrix} Q_i^r \\ 0 \\ 0 \end{bmatrix} + \xi \begin{bmatrix} 0 \\ Q_i^{\rho} \\ 0 \end{bmatrix} + \xi \begin{bmatrix} 0 \\ 0 \\ \hat{Q}_i^{\rho b} \end{bmatrix} \end{array} \right] \right\|_{\infty} =: \gamma_i^{\text{opt}} \quad (12)$$

for $i = 1, \dots, n$, where $\hat{Q}_i := \begin{bmatrix} Q_i^{rT} & Q_i^{\rho T} & \hat{Q}_i^{\rho b T} \end{bmatrix}^T$. Note that $\gamma^{\text{opt}} = \max_i(\gamma_i^{\text{opt}})$, which means that an optimal (respectively, suboptimal) solution to (11) (equivalently to (9)) is obtained by combining the optimal (respectively, suboptimal) solutions to the problems in (12).

The problems defined in (12) are similar to the problem considered in [4]. As discussed in [4], due to the multiple delays involved, there is no known approach to find optimal solutions to the problems in (12) (e.g., see [3]). Therefore, as it was done in [4], we will decompose the problems (12) into subproblems involving single delays and find a suboptimal solution to each problem in (12). For this, we introduce positive scalars, $\alpha_{i,j}^r$, $\alpha_{k,i}^{\rho}$, and $\alpha_{i,k}^{\rho b}$ ($i = 1, \dots, n$, $j = 1, \dots, n_i$, $k = 1, \dots, n$, $i \neq k$), satisfying

$$\sum_{j=1}^{n_i} \alpha_{i,j}^r + \sum_{j=1, j \neq i}^n \alpha_{j,i}^{\rho} + \sum_{j=1, j \neq i}^n \alpha_{i,j}^{\rho b} = 1, \quad i = 1, \dots, n. \quad (13)$$

The left-hand side of (12) can now be written as

$$\begin{aligned} & \inf_{\hat{Q}_i \in \mathcal{H}^{\infty}} \left\| \sum_{j=1}^{n_i} \left[\begin{array}{c} W_1 \alpha_{i,j}^r (1 - \frac{1}{\alpha_{i,j}^r} P_{i,j}^r Q_{i,j}^r) \\ \xi Q_{i,j}^r \end{array} \right] + \right. \\ & \left. \sum_{j=1, j \neq i}^n \left[\begin{array}{c} W_1 \alpha_{j,i}^{\rho} (1 - \frac{1}{\alpha_{j,i}^{\rho}} P_{j,i}^{\rho} Q_{j,i}^{\rho}) \\ \xi Q_{j,i}^{\rho} \end{array} \right] + \right. \\ & \left. \sum_{j=1, j \neq i}^n \left[\begin{array}{c} W_1 \alpha_{i,j}^{\rho b} (1 - \frac{1}{\alpha_{i,j}^{\rho b}} P_{i,j}^{\rho b} Q_{i,j}^{\rho b}) \\ \xi Q_{i,j}^{\rho b} \end{array} \right] \right\|_{\infty} \end{aligned} \quad (14)$$

where $P_i^r =: \begin{bmatrix} P_{i,1}^r & \dots & P_{i,n_i}^r \end{bmatrix}$, $P_i^{\rho} =: \begin{bmatrix} P_{1,i}^{\rho} & \dots & P_{i-1,i}^{\rho} & P_{i+1,i}^{\rho} & \dots & P_{n,i}^{\rho} \end{bmatrix}$, $\hat{P}_i^{\rho b} =: \begin{bmatrix} P_{i,1}^{\rho b} & \dots & P_{i,i-1}^{\rho b} & P_{i,i+1}^{\rho b} & \dots & P_{i,n}^{\rho b} \end{bmatrix}$, and Q_i^r , Q_i^{ρ} and $\hat{Q}_i^{\rho b}$ has the same structure as P_i^r , P_i^{ρ} and $\hat{P}_i^{\rho b}$ respectively (with P replaced by Q). Therefore, we define the following problems, each of which involves a single delay

$$\inf_{Q_{i,j}^r \in \mathcal{H}^{\infty}} \left\| \left[\begin{array}{c} W_1 \alpha_{i,j}^r (1 - \frac{1}{\alpha_{i,j}^r} P_{i,j}^r Q_{i,j}^r) \\ \xi Q_{i,j}^r \end{array} \right] \right\|_{\infty} =: \gamma_{i,j}^r \quad (15)$$

$$\inf_{Q_{j,i}^{\rho} \in \mathcal{H}^{\infty}} \left\| \left[\begin{array}{c} W_1 \alpha_{j,i}^{\rho} (1 - \frac{1}{\alpha_{j,i}^{\rho}} P_{j,i}^{\rho} Q_{j,i}^{\rho}) \\ \xi Q_{j,i}^{\rho} \end{array} \right] \right\|_{\infty} =: \gamma_{i,j}^{\rho} \quad (16)$$

$$\inf_{Q_{i,j}^{\rho b} \in \mathcal{H}^{\infty}} \left\| \left[\begin{array}{c} W_1 \alpha_{i,j}^{\rho b} (1 - \frac{1}{\alpha_{i,j}^{\rho b}} P_{i,j}^{\rho b} Q_{i,j}^{\rho b}) \\ \xi Q_{i,j}^{\rho b} \end{array} \right] \right\|_{\infty} =: \gamma_{i,j}^{\rho b} \quad (17)$$

where $j = 1, \dots, n_i$ for the problem defined in (15), $j = 1, \dots, n$, $j \neq i$ for the problems defined in (16) and (17). Note that, a suboptimal solution to (12) can be obtained by combining optimal solutions of (15)–(17), since $\gamma_i^{\text{opt}} \leq \sum_{j=1}^{n_i} \gamma_{i,j}^r + \sum_{j=1, j \neq i}^n \gamma_{j,i}^{\rho} + \sum_{j=1, j \neq i}^n \gamma_{i,j}^{\rho b}$.

Using the results of [5], the optimal solution to each of the problems in (15)–(17) can be described as $Q_{i,j} = \frac{C_{i,j}}{1 + \frac{1}{\alpha_{i,j}} C_{i,j} P_{i,j}}$ and $C_{i,j}(s) = \kappa_{i,j} \left(\frac{sh_{i,j} - k_{i,j}}{sh_{i,j}} \right) \frac{1}{1 + F_{i,j}(sh_{i,j})}$, where

the superscript \cdot represents r , ρ or ρb , where $F_{i,j}^{\cdot}$ is a finite impulse response filter and $\kappa_{i,j}$ and $k_{i,j}$ are constants to be calculated as in [4] (we can not show the details here due to space limitations). $Q_{i,j}$'s are now substituted back to obtain Q . Once Q is found, from (10), we obtain

$$\hat{K} = \begin{bmatrix} \hat{K}^r \\ \hat{K}^\rho \\ \hat{K}^{\rho b} \end{bmatrix} = Q[I - \check{P}Q]^{-1}. \text{ Then, our controller is}$$

obtained as $K = P^\dagger \hat{K}$, where P^\dagger is a left inverse of \hat{P} . Using $P^\dagger = \begin{bmatrix} I_m & 0 & 0 \\ 0 & \sqrt{2}I_{n(n-1)} & 0 \end{bmatrix}$, we obtain

$$K = \begin{bmatrix} \hat{K}^r \\ \sqrt{2}\hat{K}^\rho \end{bmatrix} = \begin{bmatrix} K_{11}^r \\ \vdots \\ K_{1n_1}^r \\ \vdots \\ 0 \\ \vdots \\ K_{nn}^r \\ 0 \\ \vdots \\ K_{n1}^\rho \\ \vdots \\ 0 \\ \vdots \\ K_{1n}^\rho \\ \vdots \\ 0 \\ \vdots \\ K_{(n-1)n}^\rho \end{bmatrix} \quad (18)$$

where

$$K_{i,j}^r = \frac{C_{i,j}^r}{1 + \frac{1}{\alpha_{i,j}^r} C_{i,j}^r P_{i,j}^r} \begin{pmatrix} 1 - \sum_{k=1}^{n_i} \frac{C_{i,k}^r P_{i,k}^r}{1 + \frac{1}{\alpha_{i,k}^r} C_{i,k}^r P_{i,k}^r} \\ \sum_{k=1, k \neq i}^n \frac{C_{k,i}^\rho P_{k,i}^\rho}{1 + \frac{1}{\alpha_{k,i}^\rho} C_{k,i}^\rho P_{k,i}^\rho} \\ \sum_{k=1, k \neq i}^n \frac{C_{i,k}^{\rho b} P_{i,k}^{\rho b}}{1 + \frac{1}{\alpha_{i,k}^{\rho b}} C_{i,k}^{\rho b} P_{i,k}^{\rho b}} \end{pmatrix}^{-1} \quad (19)$$

$$K_{j,i}^\rho = \frac{\sqrt{2} C_{j,i}^\rho}{1 + \frac{1}{\alpha_{j,i}^\rho} C_{j,i}^\rho P_{j,i}^\rho} \begin{pmatrix} 1 - \sum_{k=1}^{n_i} \frac{C_{i,k}^r P_{i,k}^r}{1 + \frac{1}{\alpha_{i,k}^r} C_{i,k}^r P_{i,k}^r} \\ \sum_{k=1, k \neq i}^n \frac{C_{k,i}^\rho P_{k,i}^\rho}{1 + \frac{1}{\alpha_{k,i}^\rho} C_{k,i}^\rho P_{k,i}^\rho} \\ \sum_{k=1, k \neq i}^n \frac{C_{i,k}^{\rho b} P_{i,k}^{\rho b}}{1 + \frac{1}{\alpha_{i,k}^{\rho b}} C_{i,k}^{\rho b} P_{i,k}^{\rho b}} \end{pmatrix}^{-1} \quad (20)$$

As seen from (18), the part of the controller for the i^{th} bottleneck node gets feedback only from q_i to regulate the queue length q_i by determining the flow rates $r_{i,j}$, $j = 1, \dots, n_i$, and $\rho_{k,i}$, $k = 1, \dots, n, k \neq i$. Therefore, the controller is composed of n decentralized controllers each of which can be implemented at the corresponding bottleneck node as shown in Figure 2. This controller stabilizes the nominal plant and makes the \mathcal{H}_∞ norm of the matrix in (9) less than some $\tilde{\gamma}$ (an upper bound that can be found from the γ 's of the suboptimal problems). Thus, as long as the hard constraints are satisfied, the controller stabilizes the actual plant for all variations of the time-delays

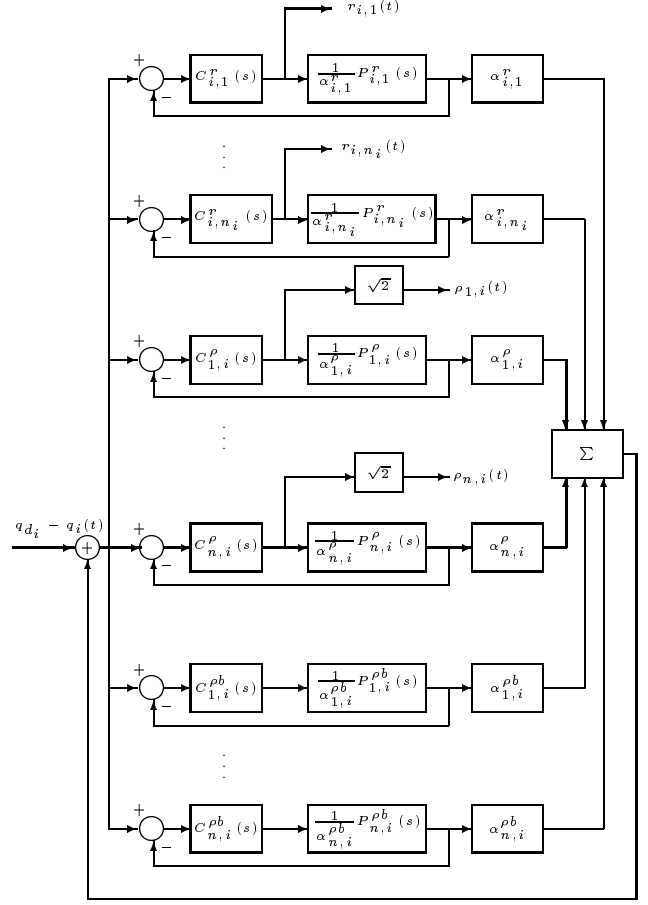


Figure 2: The implementation of the controller K_i .

satisfying $\|\delta_{i,j}^r(t)\| < \frac{\delta_{i,j}^{r+}}{\tilde{\gamma}}$, $\|\delta_{i,j}^{\rho}(t)\| < \frac{\beta_{i,j}^r}{\tilde{\gamma}}$, $\|\delta_{i,j}^{rf}(t)\| < \frac{\beta_{i,j}^r}{\tilde{\gamma}}$, $\|\delta_{j,i}^\rho(t)\| < \frac{\delta_{j,i}^{\rho+}}{\tilde{\gamma}}$, $\|\delta_{i,j}^{\rho b}(t)\| < \frac{\delta_{i,j}^{\rho b+}}{\tilde{\gamma}}$, $\|\delta_{j,i}^\rho(t)\| < \frac{\beta_{j,i}^\rho}{\tilde{\gamma}}$, and $\|\delta_{j,i}^{rf}(t)\| < \frac{\beta_{j,i}^{rf}}{\tilde{\gamma}}$.

Furthermore, assuming that the hard constraints are satisfied, that all the delays converge to constant values, and that the outgoing flow rates converge to constant values ($\lim_{t \rightarrow \infty} c_i(t) = c_{i,\infty}$, $i = 1, \dots, n$), the above controller also guarantees tracking, i.e., $\lim_{t \rightarrow \infty} q_i(t) = q_{d,i}$, $i = 1, \dots, n$. It can also be shown that (we will not show the details here due to space limitations, see [4] for the development in the single-bottleneck case) the controller satisfies a *weighted fairness* condition, where weights are defined by the scalars introduced in (13). Specifically, assuming that the hard constraints are satisfied, that all the delays converge to constant values, and that the outgoing flow rates converge to constant values, we have

$$\lim_{t \rightarrow \infty} r_{i,j}(t) = \frac{\alpha_{i,j}^r}{\bar{\alpha}_i} \left(\lim_{t \rightarrow \infty} c_i(t) + \sum_{k=1, k \neq i}^n \lim_{t \rightarrow \infty} \rho_{i,k}(t) \right), \quad (21)$$

for $j = 1, \dots, n_i$, and

$$\lim_{t \rightarrow \infty} \rho_{j,i}(t) = \frac{\sqrt{2}\alpha_{j,i}^\rho}{\bar{\alpha}_i} \left(\lim_{t \rightarrow \infty} c_i(t) + \sum_{k=1, k \neq i}^n \lim_{t \rightarrow \infty} \rho_{i,k}(t) \right), \quad (22)$$

for $j = 1, \dots, n$, $j \neq i$, where, for $i = 1, \dots, n$, $\bar{\alpha}_i := \sum_{j=1}^{n_i} \alpha_{i,j}^r + \sqrt{2} \sum_{j=1, j \neq i}^n \alpha_{j,i}^\rho$.

4 Examples

We have implemented the controller as derived above for various different networks and obtained simulations for many cases using SIMULINK. The hard constraints are also considered in the simulations. Here we show only a few examples due to space limitations. We consider a network with three bottleneck nodes. We have $n_1 = 2$, $n_2 = 3$, and $n_3 = 4$. Controller parameters and the nominal parts of the actual delays are as shown in Table 1. In the simulations, the uncertain parts of forward actual delays, $\delta_{i,j}^{rf}(t)$ and $\delta_{i,j}^{\rho f}(t)$, are taken as $0.03 \sin(\frac{\pi}{50}t)$, while the uncertain parts of backward actual delays, $\delta_{i,j}^{rb}(t)$ and $\delta_{i,j}^{\rho b}(t)$, are taken as $0.3 \sin(\frac{\pi}{50}t)$. The desired queue lengths ($q_{d,i}$'s) for the three bottleneck nodes are the same and equal to 50 packets. The capacities of the outgoing links (c_i 's) are the same and equal to 100 packets/s for all the cases except for case 3. Figures 3, 4 and 5 are the simulation results for the cases 1, 2 and 3, respectively. For all cases, the plots in (a), (b) and (c) represent the queue lengths and flow rates versus time (in s) of the sources of bottleneck nodes 1, 2 and 3, respectively, while (d) represents the flow rates versus time (in s) between the bottleneck nodes.

Case 1: The queue size at bottleneck node 1 is zero between 0 and 50 s, this period extends to about 65 s for bottleneck nodes 2 and 3. These time ranges define the period needed for the

sum of the incoming flows to exceed the capacity of the outgoing link at the bottleneck node. Note that, the steady-state flow rates satisfy fairness conditions (21)–(22).

Case 2: In this case, data supplying rates of the sources are limited as shown in Table 2. The flow rates at sources 12, 23 and 34 are saturated. Note that the controllers successfully redistribute the unused rates to the remaining sources, controllers still regulate the queue lengths in a little bit more time and a larger overshoot is observed in each of the queue lengths.

Case 3: In this case the outgoing flow rate at the first bottleneck node, c_1 , switches between 60 packets/s and 40 packets/s as a square-wave of period 200 s, while $c_2 = c_3 = 100$ packets/s. As seen from the simulation results in Figure 5, a sudden decrease in the outgoing flow rate causes a significant increase in the corresponding queue length, a sudden increase causes a sharp decrease. These sudden changes in c_1 are also sensed by the other nodes and their queue lengths and flow rates are affected to some extent but not as directly and sharply as that of node 1.

5 Conclusion

We have considered decentralized rate-based flow controller design in multi-bottleneck networks. Specifically, we have considered the \mathcal{H}^∞ problem which was set forth in [1] and solved this problem doing some manipulations and using the methods of [5]. We have also presented the implementation of the derived controller. We briefly discussed the robustness and performance (tracking and fairness) properties of the controller and presented a number of simulations.

i, j	h_{ij}^{rf}	h_{ij}^{rb}	$h_{ij}^{\rho f}$	$h_{ij}^{\rho b}$	α_{ij}^r	α_{ij}^ρ	$\alpha_{ij}^{\rho b}$	δ_{ij}^{r+}	$\delta_{ij}^{\rho+}$	$\delta_{ij}^{\rho b+}$	β_{ij}^r	β_{ij}^ρ	$\beta_{ij}^{\rho b}$	β_{ij}^{rf}	$\beta_{ij}^{\rho f}$
1, 1	0.1	1	---	---	0.1	---	---	2	---	---	0.1	---	---	0.01	---
1, 2	0.2	2	1.1	1.1	0.2	0.2	0.1	3	2	1	0.2	0.1	0.05	0.02	0.05
1, 3	---	---	1.1	1.1	---	0.12	0.1	---	2	1	---	0.1	0.05	---	0.05
2, 1	0.1	1	1.1	1.1	0.1	0.3	0.05	2	2	1	0.1	0.1	0.05	0.01	0.05
2, 2	0.2	2	---	---	0.2	---	---	3	---	---	0.2	---	---	0.02	---
2, 3	0.2	2	1.1	1.1	0.3	0.08	0.05	2	2	1	0.2	0.1	0.05	0.01	0.05
3, 1	0.1	1	1.1	1.1	0.1	0.2	0.05	2	2	1	0.1	0.1	0.05	0.01	0.05
3, 2	0.2	2	1.1	1.1	0.15	0.1	0.05	3	2	1	0.2	0.1	0.05	0.02	0.05
3, 3	0.2	2	---	---	0.2	---	---	2	---	---	0.1	---	---	0.01	---
3, 4	0.2	2	---	---	0.25	---	---	3	---	---	0.2	---	---	0.02	---

Table 1: Controller parameters

i, j	1, 1	1, 2	2, 1	2, 2	2, 3	3, 1	3, 2	3, 3	3, 4
$d_{i,j}$ (packets/second)	30	30	50	50	45	45	40	45	40

Table 2: Rate limits for Case 2

Acknowledgement

Programs used in the simulations are based on the programs previously developed by Taesam Kang, Pierre-François Quet, and Banu Ataşlar.

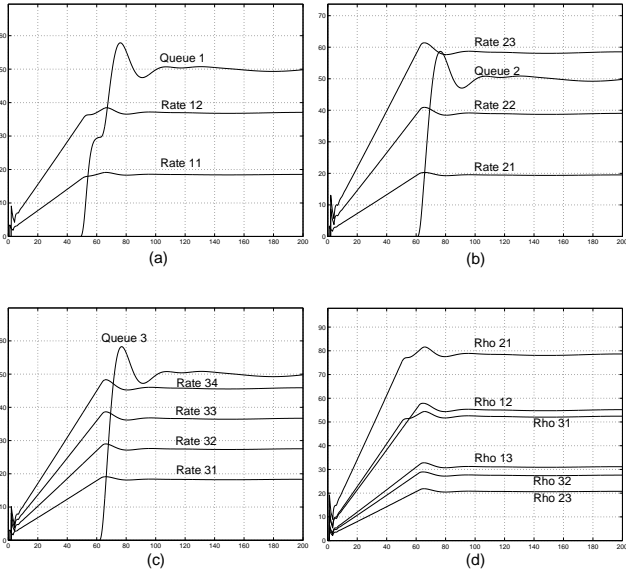


Figure 3: Simulation results for Case 1.

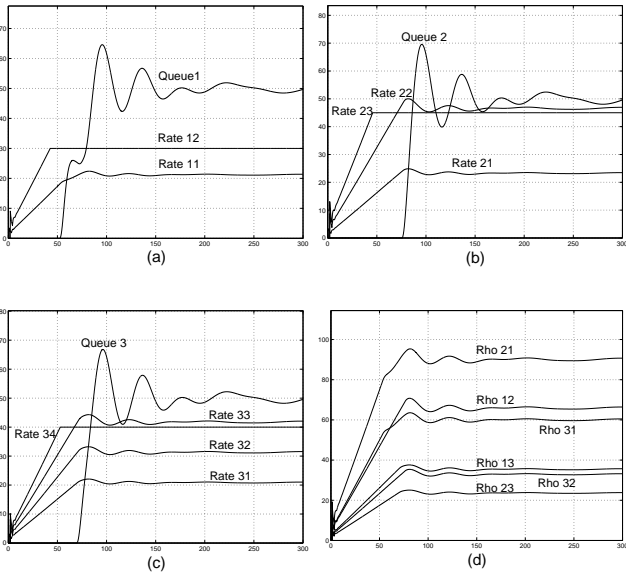


Figure 4: Simulation results for Case 2.

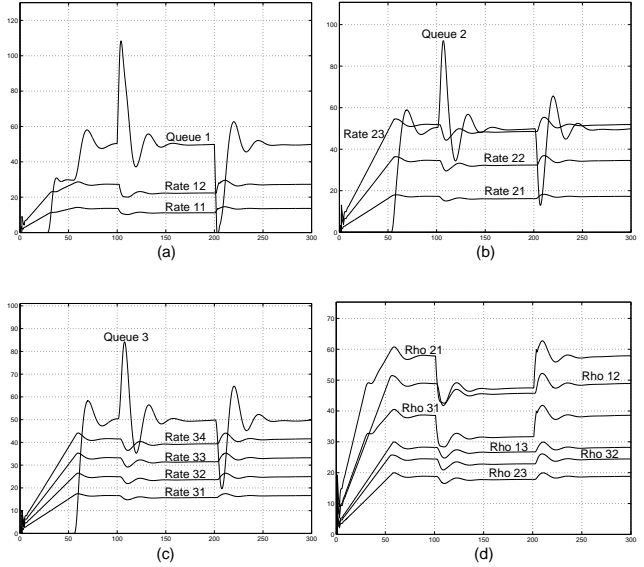


Figure 5: Simulation results for Case 3.

References

- [1] E. Biberović, A. İftar, and H. Özbay, "A solution to the robust flow control problem for networks with multiple bottlenecks," in *Proceedings of the IEEE Conference on Decision and Control*, (Orlando, FL), pp. 2303–2308, Dec. 2001.
- [2] E. Biberović, "Flow control in high-speed data communication networks," Master's thesis, Anadolu University, Eskişehir, Turkey, 2001. (In Turkish).
- [3] G. Meinsma and H. Zwart, "On \mathcal{H}^∞ control for dead-time systems," *IEEE Transactions on Automatic Control*, vol. 45, pp. 272–285, 2000.
- [4] P. Quet, B. Ataşlar, A. İftar, H. Özbay, S. Kalyanaraman, and T. Kang, "Rate-based flow controllers for communication networks in the presence of uncertain time-varying multiple time-delays," *Automatica*, vol. 38, pp. 917–928, 2002.
- [5] O. Toker and H. Özbay, " \mathcal{H}^∞ optimal and suboptimal controllers for infinite dimensional SISO plants," *IEEE Transactions on Automatic Control*, vol. 40, pp. 751–755, 1995.